

深圳市亿显国际科技有限公司 ShenZhen Yes-Display International Technology CO.,LTD.			2.8 寸液晶显示屏 2.8 Inch LCD Display Screen	
File NO.		REV	A/01	http://www.yes-display.com

SPECIFICATION FOR

Module:YS-T0280C23N-33 V1.0

Designed by	R&D Checked by	Quality Department by	Approved by

Approval by Customer:

OK

NG, Problem survey

Approved By _____

Revision Record

REV NO.	REV DATE	Contents Before Change	Contents After Change	Note
V1.0	2023/03/14	NEW ISSUE By Pan;		

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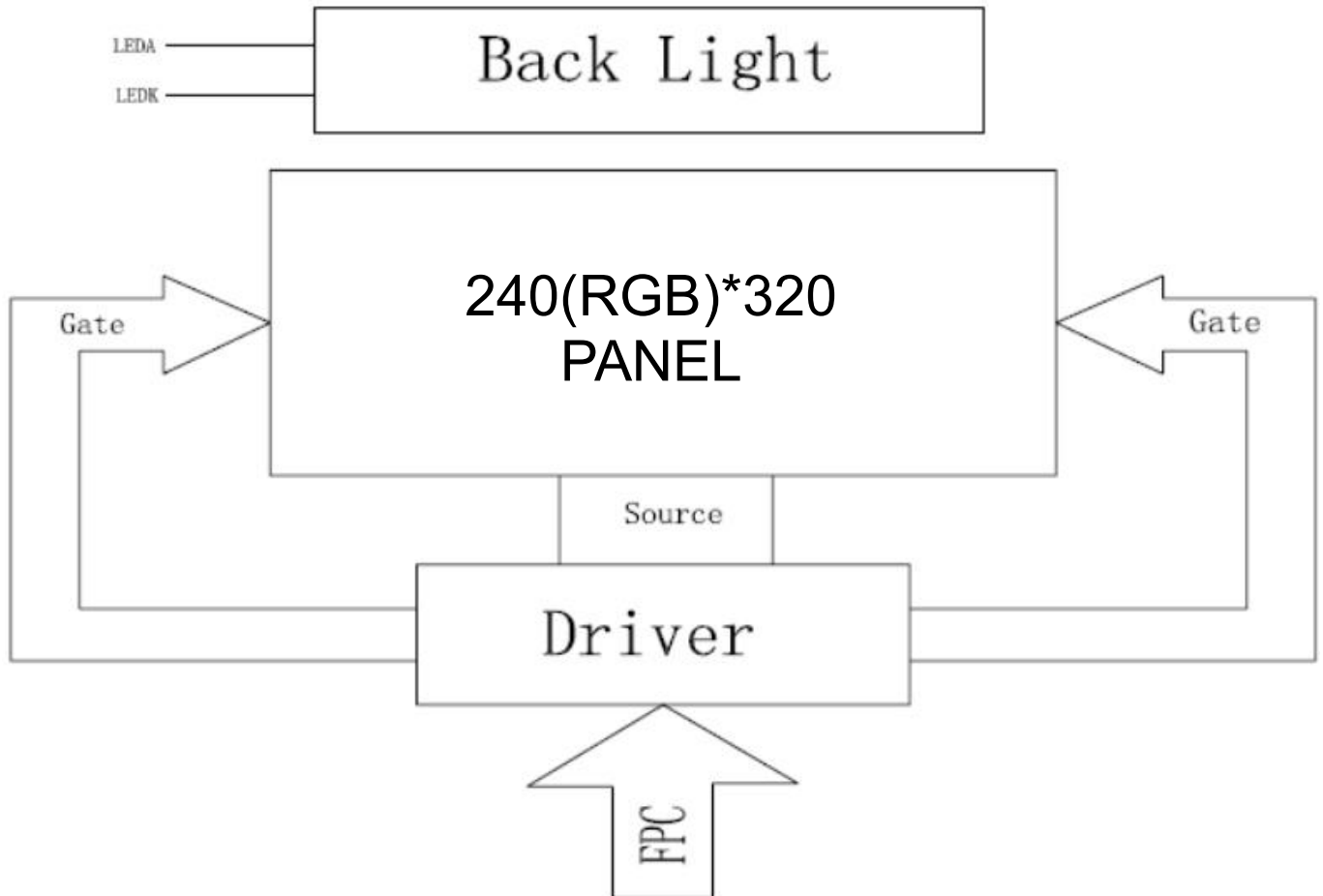
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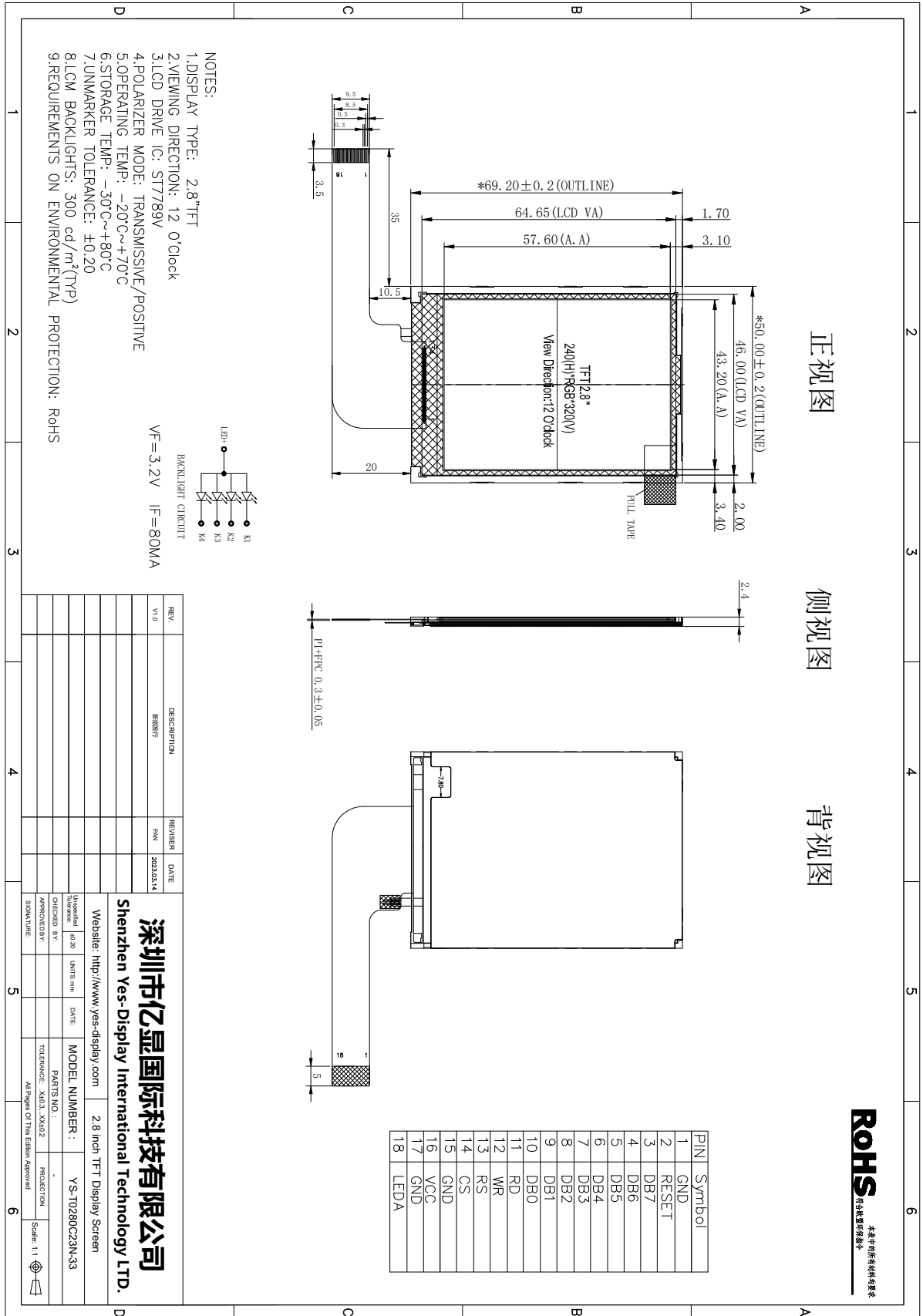
1. Technical parameters

ITEM	STANDARD VALUES	UNITS
LCD type	2.8TFT	--
Dot arrangement	240(RGB)×320	dots
Color filter array	RGB vertical stripe	--
Display mode	TN/ Transmission / Normally White	-
Eyes Viewing Direction	12 O'CLOCK	--
Driver IC	ST7789V	--
Module size	50(W)×69.2(H)×2.90(T)(Exclude FPC)	mm
Active area	43.2(W)×57.6(H)	mm
Interface	MCU_8BIT	--
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C
Back Light	White LED*4	--

2. Block Dimension



3. Outline Dimension



ROHS
符合欧盟环保指令

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4. Input terminal Pin Assignment Description

4.2 TFT Pin Description

PIN NO.	PIN NAME	DESCRIPTION
1	GND	System power ground.
2	RESET	Reset signal input terminal, active at 'L'.
3-10	DB7-DB0	Data DB7-DB0, 8BIT MCU
11	RD	8080- /8080 I -II system (RDX)
12	WR	Serves as command or parameter select.
13	RS	Serves as command or parameter select.
14	CS	Chip select input pin ("Low" enable).
15	GND	System power ground.
16	VCC	VCC power input
17	GND	System power ground.
18	LEDA	Power supply for backlight anode input terminal.

4.2 TP Pin Description

5. LCD Optical Characteristics

Item	Symbol	Condition	Specification			Unit	Remark
			Min.	Typ.	Max.		
Response time (By Quick)	Tr+Tf	$\theta = 0^\circ$	-	16		ms	Note 5
Contrast ratio	CR	$\theta = 0^\circ$	-	500	-		Note 2,6
Viewing angle	Top	$CR \geq 10$	-	50	-	deg.	Note 2,6,7
	Bottom	$CR \geq 10$	-	20	-		
	Left	$CR \geq 10$	-	45	-		
	Right	$CR \geq 10$	-	45	-		
Color chromaticity (CF only with ITO, light source is C light, CIE 1931)	Wx	$\theta = 0^\circ$		0.301			Note 3
	Wy			0.337			
	Rx			0.621			
	Ry			0.332			
	Gx			0.294			
	Gy			0.577			
	Bx			0.141			
	By			0.157			
NTSC			-	55	-	%	Note 3
Transmittance	Trans		-	6.4	-	%	Note 9

Note 1: Ambient temperature = 25°C.

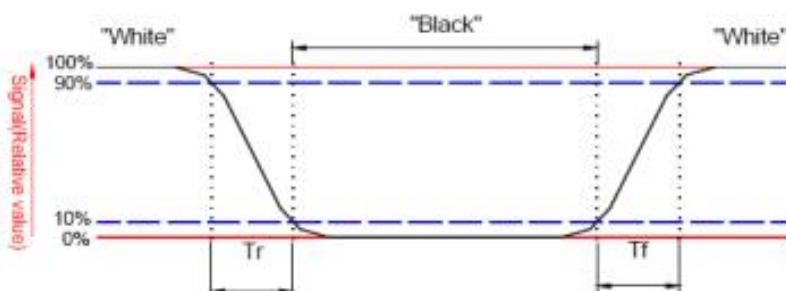
Note 2: To be measured with a viewing cone of 2° by Topcon luminance meter BM-5A.

Note 3: To be measured with Otsuta chromaticity meter LCF-2100M, CF only measure under C light simulation.

Note 4: CTC shipping status is cell without polarizer. Transmittance of Specification is cell with polarizer.
The tolerance of Transmittance is $\pm 10\%$.

Note 5: Definition of response time:

The output signals of TRD-100 are measured when the input signals are changed to "White" (falling time) and from "White" to "Black" (rising time), respectively. The interval is between the 10% and 90% of amplitudes. Refer to figure as below.

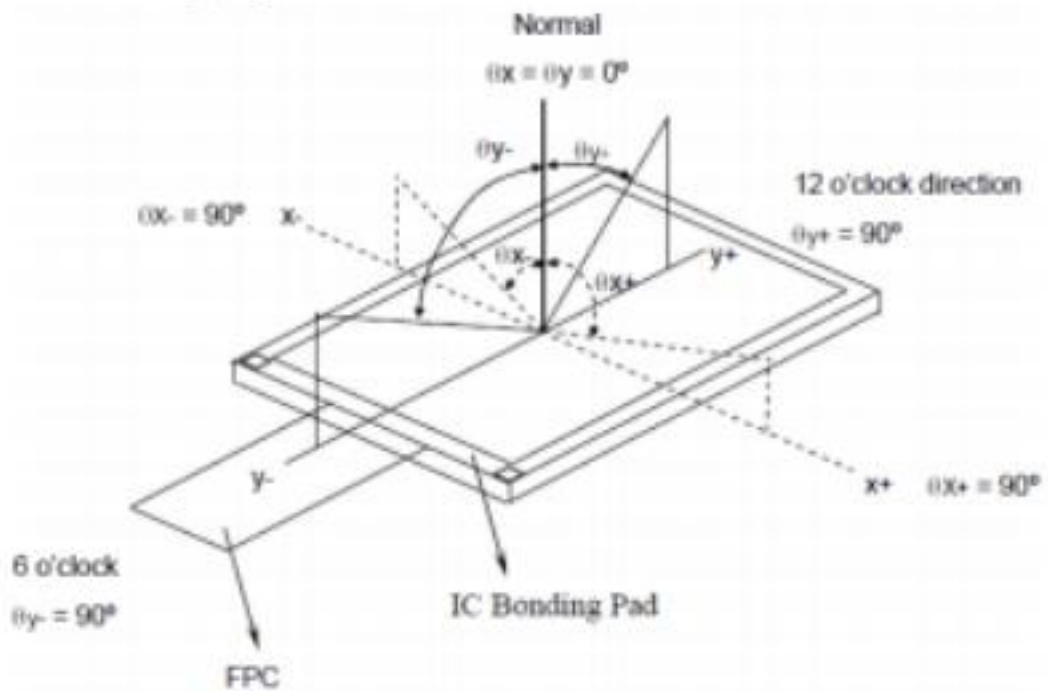


Note 6: Definition of contrast ratio:

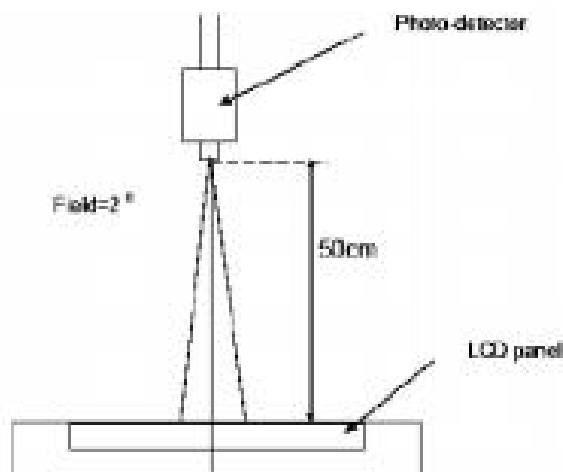
Contrast ratio is calculated by the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "white" state}}{\text{Brightness on the "black" state}}$$

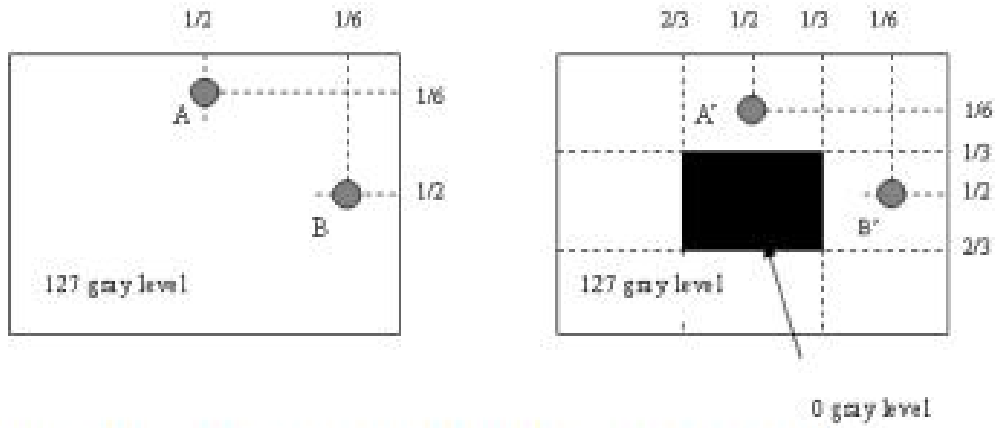
Note 7: Definition of viewing angle



Note 8: Optical characteristic measurement setup.



Note 9:



- I $(LA-LA') / LA \times 100\% = 2\% \text{ max.}$, LA and LA' are brightness at location A and A'.
- I $(LB-LB') / LB \times 100\% = 2\% \text{ max.}$, LB and LB' are brightness at location B and B'.

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6. TFT Electrical Characteristics

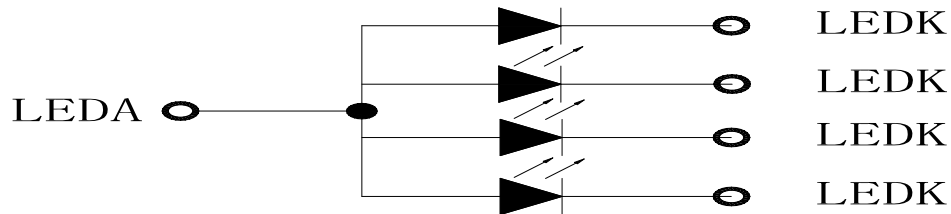
6.1 Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Logic Supply Voltage	VDD	-0.3	4.0	V
Input Voltage	V _{in}	-0.3	VDD+0.3	V
Operating Temperature	T _{OP}	-20	70	°C
Storage Temperature	T _{ST}	-30	80	°C
Storage Humidity	HD	20	90	%RH

6.2 DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Logic Supply Voltage	VDD	2.5	2.8	3.6	V	-
Input High Voltage	V _{IH}	0.7VDD	-	VDD	V	-
Input Low Voltage	V _{IL}	GND	-	0.3 VDD	V	-
Output High Voltage	V _{OH}	0.8 VDD	-	VDD	V	-
Output Low Voltage	V _{OL}	GND	-	0.2 VDD	V	-
I/O Leak Current	I _{LI}	-1	-	1	uA	-
Supply Current	IDD	-	TBD	-	mA	-

6.3 LED Backlight Characteristics



Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition
Supply Voltage	Vf	2.8	3.2	3.6	V	If=80mA
Supply Current	If	-	80	-	mA	-
Luminous Intensity for LCM	-	250	300	-	cd/m ²	If=80mA
Uniformity for LCM	-	-	60	-	%	If=80mA
Life Time	-	-	50000	-	Hr	If=80mA
Backlight Color	White					

7. Timing Characteristics

7.1 AC Characteristics

7.1.1 8080 Series MCU Parallel Interface Characteristics: 18/16/9/8-bit Bus

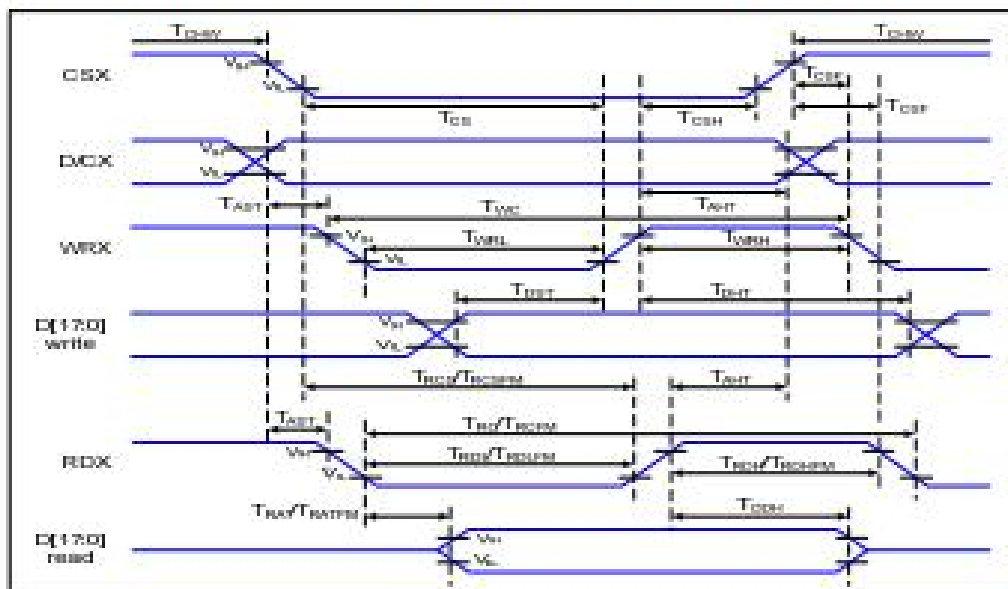


Figure 1 Parallel Interface Timing Characteristics (8080-Series MCU Interface)

VDD=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta= -30 to 70 °C

Signal	Symbol	Parameter	Min	Max	Unit	Description
DCX	T _{ASr}	Address setup time	0		ns	
	T _{AHr}	Address hold time (Write/Read)	10		ns	
CSX	T _{CHW}	Chip select "H" pulse width	0		ns	
	T _{CS}	Chip select setup time (Write)	15		ns	
	T _{RCS}	Chip select setup time (Read ID)	45		ns	
	T _{RCSFM}	Chip select setup time (Read FM)	355		ns	
	T _{CSF}	Chip select wait time (Write/Read)	10		ns	
	T _{CSH}	Chip select hold time	10		ns	
WRX	T _{WC}	Write cycle	68		ns	
	T _{WRH}	Control pulse "H" duration	15		ns	
	T _{WRL}	Control pulse "L" duration	15		ns	
RDX (ID)	T _{RC}	Read cycle (ID)	160		ns	When read ID data
	T _{RRH}	Control pulse "H" duration (ID)	90		ns	
	T _{RRL}	Control pulse "L" duration (ID)	45		ns	
RDX (FM)	T _{RCFM}	Read cycle (FM)	450		ns	When read from frame memory
	T _{RCFMH}	Control pulse "H" duration (FM)	90		ns	
	T _{RCFM}	Control pulse "L" duration (FM)	355		ns	
D[17:0]	T _{OST}	Data setup time	10		ns	For CL=30pF

T_{DHT}	Data hold time	10		ns
T_{RAT}	Read access time (IO)		40	ns
T_{RATFM}	Read access time (FM)		340	ns
T_{ODH}	Output disable time	20	80	ns

Table 4 S080 Parallel Interface Characteristics

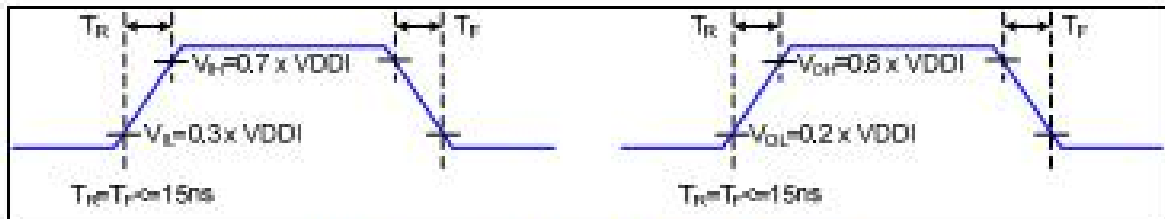


Figure 2 Rising and Falling Timing for I/O Signal

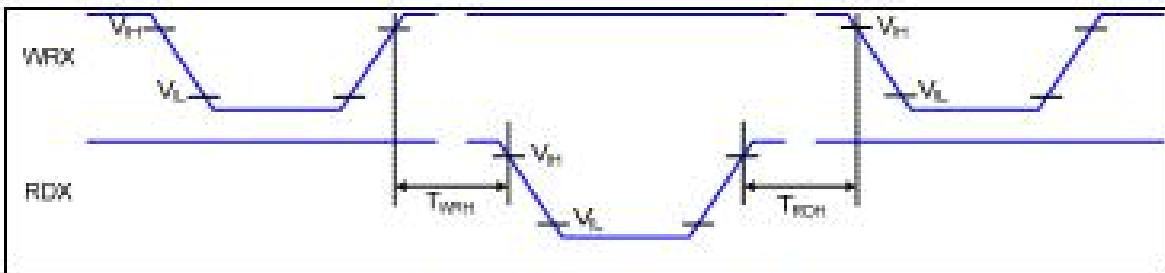


Figure 3 Write-to-Read and Read-to-Write Timing

Note: The rising time and falling time (T_R , T_F) of input signal and fall time are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for input signals.

7.1.2 Serial Interface Characteristics (3-line serial):

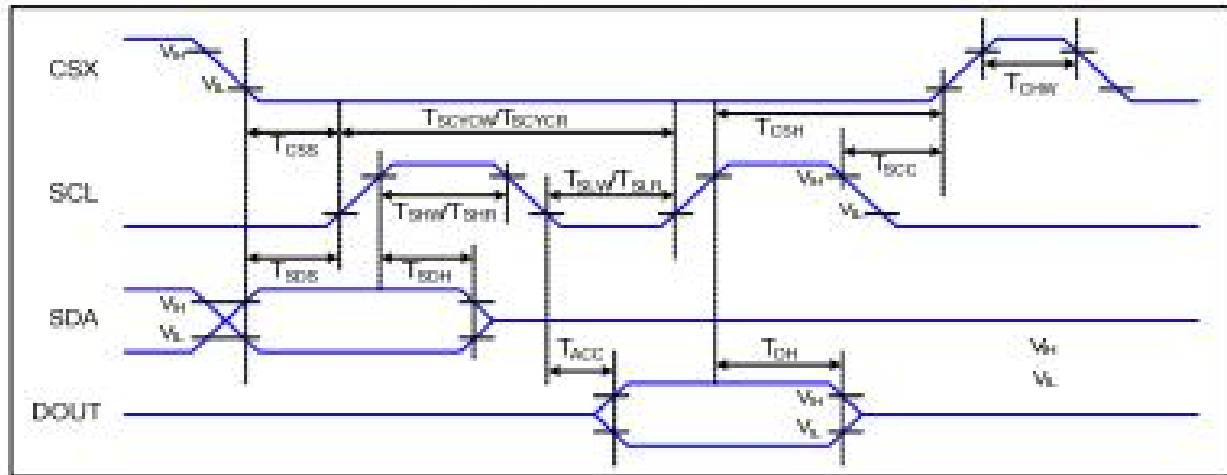


Figure 4 3-line serial Interface Timing Characteristics

V_{DD}=1.65 to 3.3V, V_{DD}=2.4 to 3.3V, AGND=DGND=0V, T_{amb}=-30 to 70 °C

Signal	Symbol	Parameter	Min	Max	Unit	Description
CSX	T _{css}	Chip select setup time (write)	15		ns	
	T _{csh}	Chip select hold time (write)	15		ns	
	T _{css}	Chip select setup time (read)	60		ns	
	T _{scch}	Chip select hold time (read)	65		ns	
	T _{chW}	Chip select "H" pulse width	40		ns	
SCL	T _{scycw}	Serial clock cycle (Write)	68		ns	
	T _{slw}	SCL "H" pulse width (Write)	15		ns	
	T _{slr}	SCL "L" pulse width (Write)	15		ns	
	T _{scycr}	Serial clock cycle (Read)	150		ns	
	T _{slr}	SCL "H" pulse width (Read)	60		ns	
	T _{slw}	SCL "L" pulse width (Read)	60		ns	
SDA (DIN)	T _{sdw}	Data setup time	10		ns	
	T _{sdh}	Data hold time	10		ns	
DOUT	T _{acc}	Access time	10	50	ns	For maximum CL=30pF
	T _{dch}	Output disable time	15	50	ns	For minimum CL=8pF

Table 5 3-line serial Interface Characteristics

Note: The rising time and falling time (R, T) of input signal are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of V_{DD} for input signals.

7.1.3 Serial Interface Characteristics (4-line serial):

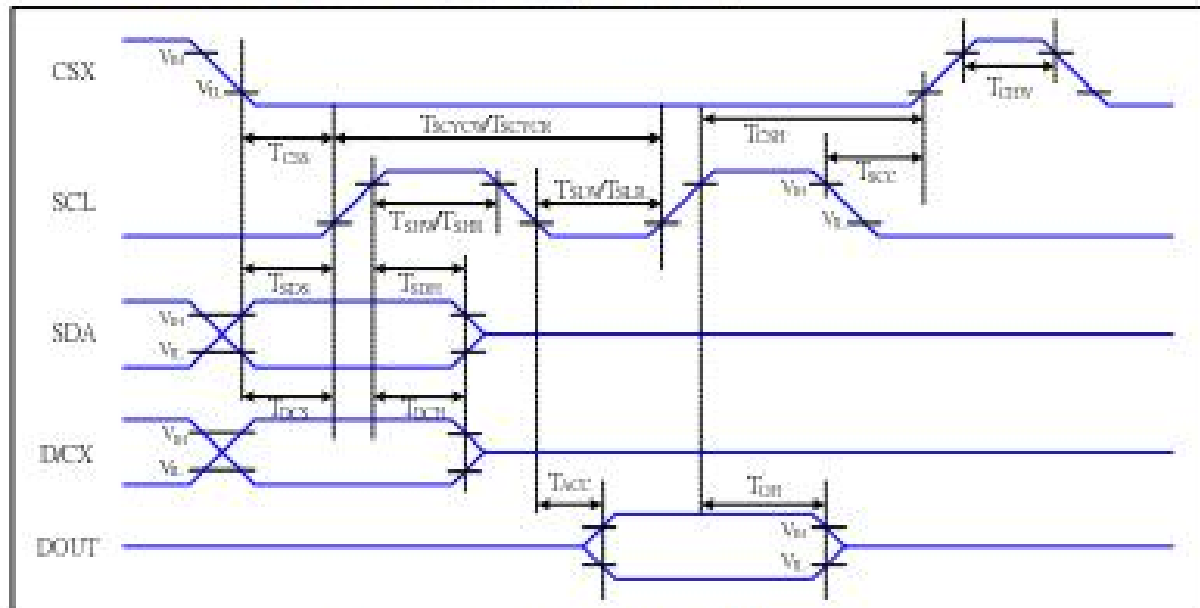


Figure 5 4-line serial Interface Timing Characteristics

V_{DD}=1.65 to 3.3V, V_{DD}=2.4 to 3.3V, AGND=DGND=0V, T_{amb}=-30 to 70 °C

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
CSX	T _{css}	Chip select setup time (write)	15		ns	
	T _{sch}	Chip select hold time (write)	15		ns	
	T _{css}	Chip select setup time (read)	60		ns	
	T _{sch}	Chip select hold time (read)	65		ns	
	T _{chW}	Chip select "H" pulse width	40		ns	
SCL	T _{scw}	Serial clock cycle (Write)	66		ns	-write command & data ram
	T _{slw}	SCL "H" pulse width (Write)	15		ns	
	T _{slr}	SCL "L" pulse width (Write)	15		ns	
	T _{scr}	Serial clock cycle (Read)	150		ns	-read command & data ram
	T _{sh}	SCL "H" pulse width (Read)	60		ns	
	T _{slr}	SCL "L" pulse width (Read)	60		ns	
DCX	T _{dcs}	DCX setup time	10		ns	
	T _{dch}	DCX hold time	10		ns	
SDA (DIN)	T _{sdS}	Data setup time	10		ns	
	T _{sdH}	Data hold time	10		ns	
DOUT	T _{acc}	Access time	10	50	ns	For maximum CL=30pF
	T _{dh}	Output disable time	15	50	ns	For minimum CL=8pF

Table 6 4-line serial Interface Characteristics

Note: The rising time and falling time (R, F) of input signal are specified at 15ns or less. Logic high and low levels are specified as

7.1.4 RGB Interface Characteristics:

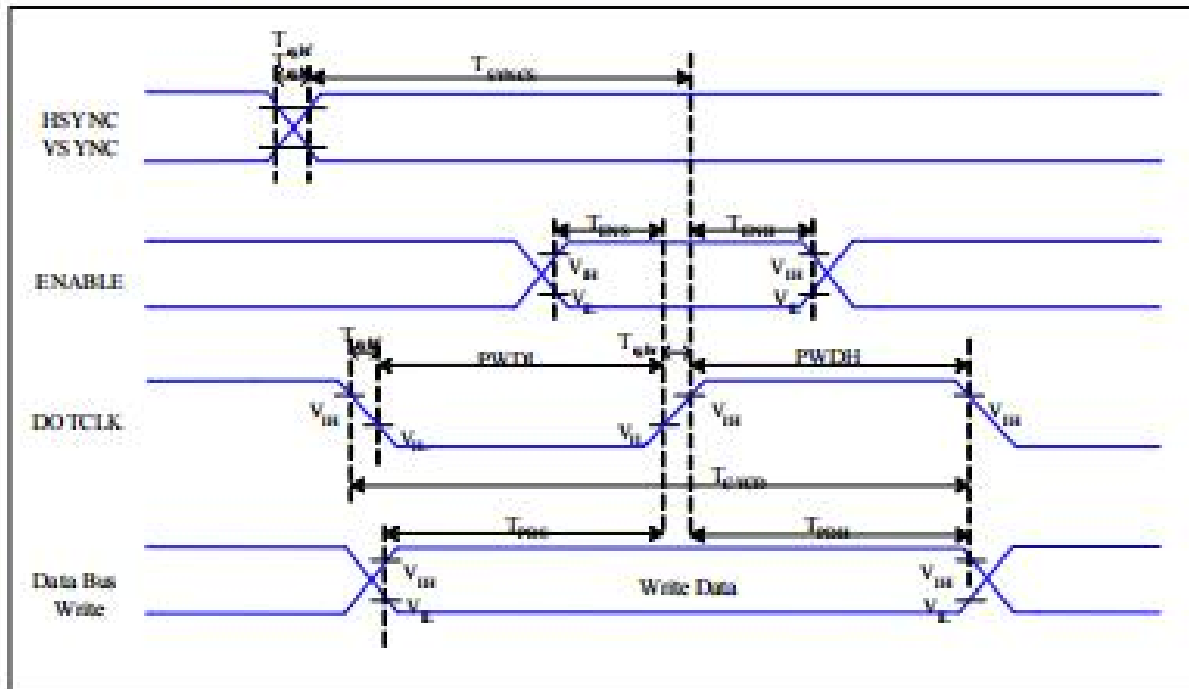


Figure 4 RGB Interface Timing Characteristics

$VDDH=1.65$ to $3.3V$, $VDD=2.4$ to $3.3V$, $AGND=OGND=0V$, $T_a=-30 \sim 70$ °C

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	T_{svics}	VSYNC, HSYNC Setup Time	30	-	ns	
ENABLE	T_{ens}	Enable Setup Time	25	-	ns	
	T_{enth}	Enable Hold Time	25	-	ns	
DOTCLK	PWDH	DOTCLK High-level Pulse Width	60	-	ns	
	PWDL	DOTCLK Low-level Pulse Width	60	-	ns	
	T_{cyc}	DOTCLK Cycle Time	120	-	ns	
	T_{ighr} , T_{ighf}	DOTCLK Rise/Fall time	-	20	ns	
DB	T_{pos}	PD Data Setup Time	50	-	ns	
	T_{pdh}	PD Data Hold Time	50	-	ns	

Table 7 18/16 Bits RGB Interface Timing Characteristics

7.1.5 Reset Timing:

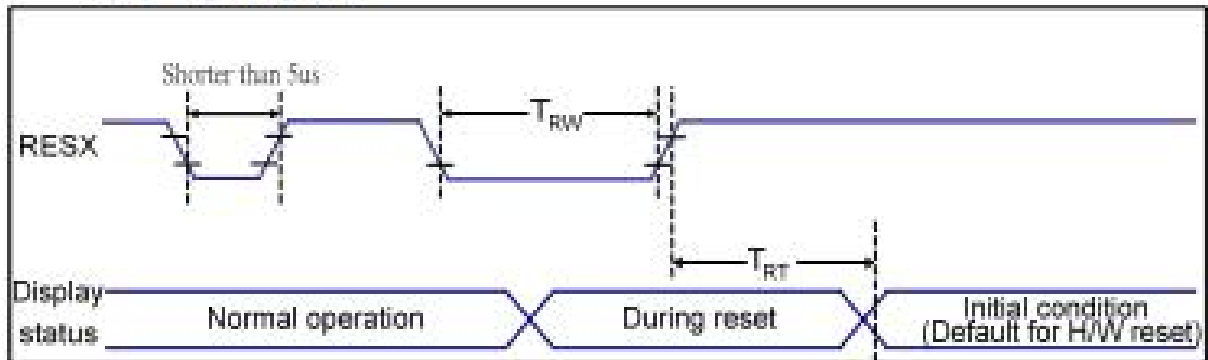


Figure 7 Reset Timing

VDD1=1.65 to 3.3V, VDD=2.4 to 3.3V, A/GND=D/GND=0V, $T_a = -30 \sim 70$ °C

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
-			120 (Note 1, 6, 7)	ms	

Table 8 Reset Timing

Notes:

- The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (TRT) within 5 ms after a rising edge of RESX.
- Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

- During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode.) and then return to Default condition for Hardware Reset.

- Spike Rejection also applies during a valid reset pulse as shown below:

7.2 TP Timing Characteristics

TBD

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8. Inspection Standard

8.1 Incoming Inspection and Standard:

The below incoming inspection are applied to the TFT LCM Modules supplied by ShenZhen Yes-Display International Technology CO.,LTD. The customers should inspect the LCM within 14 days after receiving the goods. The result of inspection should be notified to the Seller in the writing copy promptly, if the customer do not send them within 14 days, the seller has the right to judge as acceptance of goods. The inspection lot size is treated as the quantity per shipment and per model. The sampling plan shall be inspected under MIL-STD015E in Level II by single sampling. The acceptable quality level (AQL) are categorized as below grades:

CRITICAL= 0.4%, MAJOR= 0.65%, MINOR= 1.5%

8.2 Inspection condition and Warranty policy:

The delivered LCM should be stored properly, ideally under climate-controlled environment at 25 (±5) degree Celsius as well as 60% (±10) Relative Humidity. The LCM shall be inspected in the viewing angle of 45 degree from the four major angles (U/D/L/R) under the single fluorescent lamp of 20W (equal to 300 to 500 lux). For warranty, ShenZhen Yes-Display International Technology CO.,LTD. will provide 12 months of warranty period as standard, and provide the new replacement for the defective products which belong to the Seller's responsibility verified by the quality department.

8.3 Inspection Criteria:

8.3.1 Critical defect

Item No.	Inspection content	Judgement
8.3.1.1	Functional defects	No display, abnormal display, short circuit, missing line, off-contrast and chromaticity, Touch Panel non-function
8.3.1.2	Model mixed	Other model mixed

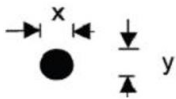
8.3.2 Major defect:


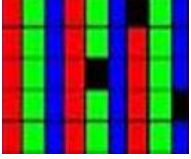
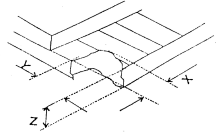
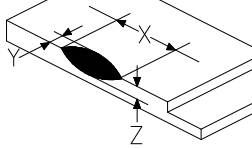
Item No.	Inspection content	Judgement
8.3.2.1	Product indication	Missing model no. and wrong model no. is indicated on the LCM.
8.3.2.2	Glass cracking	The LCD and touch panel glass crack or breakage

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8.3.2.3	Missing component	The function component missing such as connector, cable, etc.
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8.3.3 Minor defect (LCD) :

Item No.	Inspection content	Judgement												
8.3.3.1	Black/White spot Foreign particles Dust in the cell	$\phi = (x+y) / 2$  <table border="1"> <thead> <tr> <th>Diameter (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.1$</td> <td>Ignore</td> </tr> <tr> <td>$0.1 < \Phi \leq 0.25$</td> <td>3 (Distance>5mm)</td> </tr> <tr> <td>$0.25 < \Phi$</td> <td>Not allowed</td> </tr> </tbody> </table>	Diameter (mm)	Acceptable Q'ty	$\Phi \leq 0.1$	Ignore	$0.1 < \Phi \leq 0.25$	3 (Distance>5mm)	$0.25 < \Phi$	Not allowed				
Diameter (mm)	Acceptable Q'ty													
$\Phi \leq 0.1$	Ignore													
$0.1 < \Phi \leq 0.25$	3 (Distance>5mm)													
$0.25 < \Phi$	Not allowed													
8.3.3.2	Linear defect Black/white line Black/white scratch	<table border="1"> <thead> <tr> <th>Length(mm)</th> <th>Width (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td></td> <td>$W \leq 0.03$</td> <td>Ignore</td> </tr> <tr> <td>$L \leq 5.0$</td> <td>$0.03 < W \leq 0.07$</td> <td>3</td> </tr> <tr> <td></td> <td>$0.07 < W$</td> <td>Follow 8.3.3.1</td> </tr> </tbody> </table>	Length(mm)	Width (mm)	Acceptable Q'ty		$W \leq 0.03$	Ignore	$L \leq 5.0$	$0.03 < W \leq 0.07$	3		$0.07 < W$	Follow 8.3.3.1
Length(mm)	Width (mm)	Acceptable Q'ty												
	$W \leq 0.03$	Ignore												
$L \leq 5.0$	$0.03 < W \leq 0.07$	3												
	$0.07 < W$	Follow 8.3.3.1												
8.3.3.3	Polarizer Bubbles Dent on polarizer	<table border="1"> <thead> <tr> <th>Diameter (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.2$</td> <td>Ignore</td> </tr> <tr> <td>$0.2 < \Phi \leq 0.5$</td> <td>2 (Distance>5mm)</td> </tr> <tr> <td>$0.5 < \Phi$</td> <td>Not allowed</td> </tr> </tbody> </table>	Diameter (mm)	Acceptable Q'ty	$\Phi \leq 0.2$	Ignore	$0.2 < \Phi \leq 0.5$	2 (Distance>5mm)	$0.5 < \Phi$	Not allowed				
Diameter (mm)	Acceptable Q'ty													
$\Phi \leq 0.2$	Ignore													
$0.2 < \Phi \leq 0.5$	2 (Distance>5mm)													
$0.5 < \Phi$	Not allowed													

<p>8.3.3.4</p>	<p>Electrical Defect Dot</p>	<p>Bright dot and Dark dot definition:</p>  <p>or</p>  <p>(Two adjacent dot)</p> <p>Inspection pattern: black, white, red, green, and blue screen.</p> <table border="1" data-bbox="730 730 1441 913"> <thead> <tr> <th>Items</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>Bright dot</td> <td>$N \leq 4$ (Distance >5mm)</td> </tr> <tr> <td>Dark dot</td> <td>$N \leq 4$ (Distance >5mm)</td> </tr> </tbody> </table>	Items	Acceptable Q'ty	Bright dot	$N \leq 4$ (Distance >5mm)	Dark dot	$N \leq 4$ (Distance >5mm)
Items	Acceptable Q'ty							
Bright dot	$N \leq 4$ (Distance >5mm)							
Dark dot	$N \leq 4$ (Distance >5mm)							
<p>8.3.3.5</p>	<p>Glass Defect- Corner chipping</p>	 <table border="1" data-bbox="730 1104 1441 1379"> <thead> <tr> <th>Size(mm)</th> <th>Judgement</th> </tr> </thead> <tbody> <tr> <td> $X \leq 3\text{mm}$, $Y \leq S$, $Z \leq T$ (S= ITO length, T=Single glass thickness) </td> <td>Accept</td> </tr> </tbody> </table>	Size(mm)	Judgement	$X \leq 3\text{mm}$, $Y \leq S$, $Z \leq T$ (S= ITO length, T=Single glass thickness)	Accept		
Size(mm)	Judgement							
$X \leq 3\text{mm}$, $Y \leq S$, $Z \leq T$ (S= ITO length, T=Single glass thickness)	Accept							
<p>8.3.3.6</p>	<p>Glass Defect- Side fragment</p>	 <table border="1" data-bbox="730 1574 1441 1787"> <thead> <tr> <th>Size(mm)</th> <th>Judgement</th> </tr> </thead> <tbody> <tr> <td> $X \leq 2\text{ mm}$, $Y \leq \text{border edge}$ $Z \leq T$ (T= single glass thickness) </td> <td>Accept</td> </tr> </tbody> </table>	Size(mm)	Judgement	$X \leq 2\text{ mm}$, $Y \leq \text{border edge}$ $Z \leq T$ (T= single glass thickness)	Accept		
Size(mm)	Judgement							
$X \leq 2\text{ mm}$, $Y \leq \text{border edge}$ $Z \leq T$ (T= single glass thickness)	Accept							

8.3.4 Minor defect (Touch Panel)

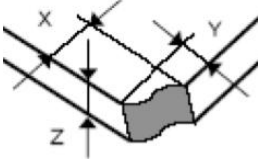
Item No.	Inspection content	Judgement
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File NO.

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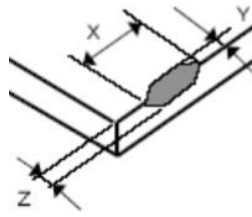
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<p>8.3.4.1</p>	<p>Scratch, dust, particles, foreign materials in “linear type”</p>	<table border="1"> <thead> <tr> <th>Size (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$W \leq 0.05\text{mm}, L \leq 10\text{mm}$</td> <td>Ignore</td> </tr> <tr> <td>$0.05\text{mm} < W \leq 0.07\text{mm}, L \leq 10\text{mm}$</td> <td>3</td> </tr> <tr> <td>$W > 0.07\text{mm}$</td> <td>Reject</td> </tr> </tbody> </table>	Size (mm)	Acceptable Q'ty	$W \leq 0.05\text{mm}, L \leq 10\text{mm}$	Ignore	$0.05\text{mm} < W \leq 0.07\text{mm}, L \leq 10\text{mm}$	3	$W > 0.07\text{mm}$	Reject
Size (mm)	Acceptable Q'ty									
$W \leq 0.05\text{mm}, L \leq 10\text{mm}$	Ignore									
$0.05\text{mm} < W \leq 0.07\text{mm}, L \leq 10\text{mm}$	3									
$W > 0.07\text{mm}$	Reject									
<p>8.3.4.2</p>	<p>Scratch, dust, particles, foreign materials in “round type”</p>	<table border="1"> <thead> <tr> <th>Diameter (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.25\text{mm}$</td> <td>Ignore</td> </tr> <tr> <td>$0.25\text{mm} < \Phi \leq 0.35\text{mm}$</td> <td>5</td> </tr> <tr> <td>$\Phi > 0.35\text{mm}$</td> <td>Reject</td> </tr> </tbody> </table>	Diameter (mm)	Acceptable Q'ty	$\Phi \leq 0.25\text{mm}$	Ignore	$0.25\text{mm} < \Phi \leq 0.35\text{mm}$	5	$\Phi > 0.35\text{mm}$	Reject
Diameter (mm)	Acceptable Q'ty									
$\Phi \leq 0.25\text{mm}$	Ignore									
$0.25\text{mm} < \Phi \leq 0.35\text{mm}$	5									
$\Phi > 0.35\text{mm}$	Reject									
<p>8.3.4.3</p>	<p>Air bubbles</p>	<table border="1"> <thead> <tr> <th>Diameter (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.2\text{mm}$</td> <td>Ignore</td> </tr> <tr> <td>$0.2\text{mm} < \Phi \leq 0.5\text{mm}$</td> <td>3</td> </tr> <tr> <td>$\Phi > 0.5\text{mm}$</td> <td>Reject</td> </tr> </tbody> </table>	Diameter (mm)	Acceptable Q'ty	$\Phi \leq 0.2\text{mm}$	Ignore	$0.2\text{mm} < \Phi \leq 0.5\text{mm}$	3	$\Phi > 0.5\text{mm}$	Reject
Diameter (mm)	Acceptable Q'ty									
$\Phi \leq 0.2\text{mm}$	Ignore									
$0.2\text{mm} < \Phi \leq 0.5\text{mm}$	3									
$\Phi > 0.5\text{mm}$	Reject									
<p>8.3.4.5</p>	<p>Scratch on printing area</p>	<table border="1"> <thead> <tr> <th>Size (mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$W \leq 0.03\text{mm}, L \leq 5\text{mm}$</td> <td>Ignore</td> </tr> <tr> <td>$0.03\text{mm} < W \leq 0.05\text{mm}, L \leq 5\text{mm}$</td> <td>3</td> </tr> <tr> <td>$W > 0.05\text{mm}$ or $L > 5\text{mm}$</td> <td>Reject</td> </tr> </tbody> </table>	Size (mm)	Acceptable Q'ty	$W \leq 0.03\text{mm}, L \leq 5\text{mm}$	Ignore	$0.03\text{mm} < W \leq 0.05\text{mm}, L \leq 5\text{mm}$	3	$W > 0.05\text{mm}$ or $L > 5\text{mm}$	Reject
Size (mm)	Acceptable Q'ty									
$W \leq 0.03\text{mm}, L \leq 5\text{mm}$	Ignore									
$0.03\text{mm} < W \leq 0.05\text{mm}, L \leq 5\text{mm}$	3									
$W > 0.05\text{mm}$ or $L > 5\text{mm}$	Reject									
<p>8.3.4.6</p>	<p>Corner chipping</p>	<div style="text-align: center;">  </div> <table border="1"> <thead> <tr> <th>Size(mm)</th> <th>Judgement</th> </tr> </thead> <tbody> <tr> <td>$X \leq 2\text{mm}, Y \leq 2\text{mm}$ $Z < 1/2T$ (T= single glass thickness)</td> <td>Accept</td> </tr> </tbody> </table>	Size(mm)	Judgement	$X \leq 2\text{mm}, Y \leq 2\text{mm}$ $Z < 1/2T$ (T= single glass thickness)	Accept				
Size(mm)	Judgement									
$X \leq 2\text{mm}, Y \leq 2\text{mm}$ $Z < 1/2T$ (T= single glass thickness)	Accept									

8.3.4.7

Edge chipping



Size(mm)	Judgement
$X \leq 3 \text{ mm}$, $Y \leq 3 \text{ mm}$ $Z \leq 1/2 T$ (T= single glass thickness)	Accept

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9. Reliability Test Conditions and Methods

9.1 Reliability Test Conditions and Methods:

NO.	TEST ITEMS	TEST CONDITION	INSPECTION AFTER TEST
①	High Temperature Storage	80°C±2°C×96Hours	Inspection after 2~4hours storage at room temperature, the samples should be free from defects: 1, Air bubble in the LCD. 2, Seal leak. 3, Non-display. 4, Missing segments. 5, Glass crack. 6, Current IDD is twice higher than initial value. 7, The surface shall be free from damage. 8, The electric characteristic requirements shall be satisfied.
②	Low Temperature Storage	-30°C±2°C×96Hours	
③	High Temperature Operating	70°C±2°C×96Hours	
④	Low Temperature Operating	-20°C±2°C×96Hours	
⑤	Temperature Cycle(Storage)	-20°C ↔ 25°C ↔ 70°C (30min) ← (5min) → (30min) 1cycle Total 10cycle	
⑥	Damp Proof Test (Storage)	50°C±5°C×90%RH×96Hours	
⑦	Vibration Test	Frequency:10Hz~55Hz~10Hz Amplitude:1.5MM X,Y,Z direction for total 3hours (packing condition test will be tested by a carton)	
⑧	Drooping Test	Drop to the ground from 1M height one time every side of carton. (packing condition test will be tested by a carton)	
⑨	ESD Test	Voltage:±8KV,R:330Ω,C:150PF,Air Mode,10times	

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REMARK:

- 1, The Test samples should be applied to only one test item.
- 2, Sample side for each test item is 5~10pcs.
- 3, For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used.
- 4, In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
- 5, EL evaluation should be accepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6, Failure Judgment Criterion: Basic Specification Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

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10. Cautions and Handling Precautions

10.1 Mounting method

The LCD panel of TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

10.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (Cl), Sulfur (S) from customer, Responsibility is on customer.

10.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

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10.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

10.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

10.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.

[It is recommended to store them as they have been contained in the inner container at the time of delivery from us

10.7 Safety

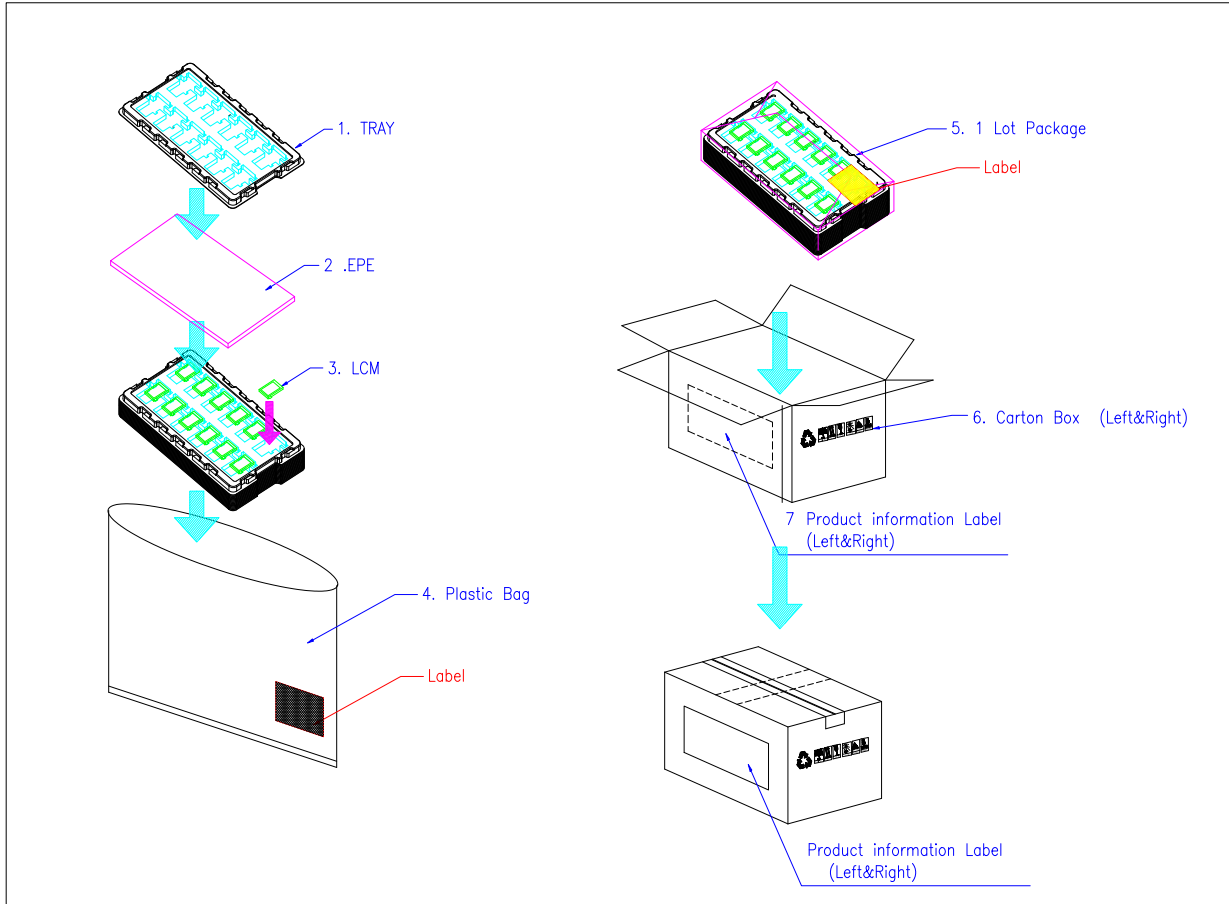
- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.

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- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

11. Packing Method

11.1 Method



11.2 Packing Label

TBD